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Applicant(s): David Dice et al.

Title: QUICKLY REACQUIRABLE LOCKS

Application No.: 10/669,948 Filed: September 24, 2003

Examiner: William D. Thomson Group Art Unit: 2194

Atty. Docket No.: 004-8728 Confirmation No.: 8314

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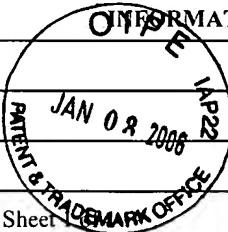
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Respectfully submitted,



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PTO/SB/08B (Substitute for form 1449/PTO)		Attorney Docket No.: 004-8728	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: David Dice	
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		Examiner Name: William D. Thomson	
		Date Submitted: December 28, 2005	
NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	MASSALIN, H., et al., "A Lock-Free Multiprocessor OS Kernel," Technical Report TR CUCS-005-9, Columbia University, New York, NY, 1991, 21 pages.	
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	5	ATTIYA, Hagit, et al., "Are Wait-Free Algorithms Fast?" <i>Journal of the ACM</i> , 41(4):725-763, July 1994.	
	6	LAMARCA, A., "A performance evaluation of lock-free synchronization protocols," <i>Proceedings of the 13th Annual ACM Symposium on Principles of Distributed Computing</i> , pp. 130-140, ACM Press, New York, NY, 1994.	
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Examiner Signature		Date Considered	

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